As an inventor named below or on any attached continuation page, I hereby declare that:

My residence, post office address and citizenship are as stated next to my name.

I believe that I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled LOCAL INTERCONNECT STRUCTURE FOR INTEGRATED CIRCUIT DEVICES, SOURCE STRUCTURE FOR THE SAME, AND METHOD FOR FABRICATING THE SAME, the specification of which (check one):

⊠ is attached hereto.				
was filed on as Uni	ted States application serial no.	and was amended on		
was filed on as PC	international application no.	and was amended under PCT Article 19 on	•	
I hereby state that I have reviewed and referred to above.	I understand the contents of the above-ide	entified specification, including the claims, as amen	ided by any ar	me nd ment
I acknowledge the duty to disclose to matter claimed in this application, as "ma	he U.S. Patent and Trademark Office all	information known to me to be material to the pate	ntability of th	e subject
attached continuation page and have also	tional application(s) designating at least of identified below and on any attached confidence.	9 (a)-(d) or § 365(b) of any foreign application(s) for one country other than the United States of America tinuation page any foreign application for patent or United States of America having a filing date before	listed below a	and on any
Prior foreign/PCT application(s):				
(number)			Priority Cla	aimed
(number)	(country)	(day/month/year filed)	Yes	No
(number)	(country)	(day/month/year filed) ed States application(s) or § 365(c) of PCT internati	Yes	No
duty to disclose to the U.S. Patent and Tra Regulations § 1.56 which became available	or application in the manner provided by demark Office all information known to	tion page and, insofar as the subject matter of each the first paragraph of Title 35, United States Code me to be material to patentability as defined in Title plication and the national or PCT international filing	, § 112, I ack	nowledge the
(application serial no.)	(filing date)	(status - pending, patented or a	bandoned)	
(application serial no.)	(filing date)	(status - pending, patented or a	handonad	
I hereby claim the benefit under Title 35	United States Code, § 119(e) of any Un	ited States provisional application(s) listed below:	<i>ванионеи)</i>	
(provisional application no.)	(filing date)	······································		
(provisional application no.)	(filing date)	·······		
I hereby appoint the following Registere therewith:	d Practitioners to prosecute this application	on and to transact all business in the Patent and Tra	demark Offic	e connected
David V. Trask, Reg. No. 22,012 Laurence B. Bond, Reg. No. 30,549 Allen C. Turner, Reg. No. 33,041 Keith L. Hargrove, Reg. No. 34,836 Edgar R. Cataxinos, Reg. No. 39,931 Kenneth Booth, Reg. No. P-43,342	William S. Britt, Reg. No. 20,969 Joseph A. Walkowski, Reg. No. 28,7 Kent S. Burningham, Reg. No. 30,45 Robert G. Winkle, Reg. No. 37,474 Kenneth E. Horton, Reg. No. 39,481 Michael L. Lynch, Reg. No. 30,871	Thomas J. Rossa, Reg. No. 26,799 James R. Duzan, Reg. No. 28,393 Julie K. Morriss, Reg. No. 33,263 Patrick McBride, Reg. No. 39,295 Brick G. Power, Reg. No. 38,581 Lia M. Pappas, Reg. No. 34,095		
P.	seph A. Walkowski, telephone no. (801) RASK, BRITT & ROSSA O. BOX 2550 It Lake City, Utah 84110	532-1922.		
		nd that all statements made on information and belie ements and the like so made are punishable by fine false statements may jeopardize the validity of the a		
Full name of first joint inventor: Jigish D. Inventor's signature Residence: Boise, Idaho Citizenship: India Post Office Address: 197 F. Mellerd Drive	ec	Date3/31198		-
Post Office Address: 197 E. Mallard Drive,	#251 Boise, ID 83706			

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DECLARATION FOR PATENT APPLICATION

(continuation page)

Invention title: LOCAL INTERCONNECT STRUCTURE FOR INTEGRATED CIRCUIT DEVICES, SOURCE STRUCTURE FOR THE SAME, AND METHOD FOR FABRICATING THE SAME

Inventor name(s) appearing on first declaration page: Jigish D. Trivedi

Additional original, first and joint inventor(s):

Full name of second joint inventor: Michael P. Violette
Inventor's signature

Date

3/23/98

Residence: Boise, Idaho

Citizenship: USA

Post Office Address: 1088 East Fleetwood Court, Boise, ID 83706

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Jigish D. Trivedi et al.

Serial No.: 09/055,056

Filed: April 3, 1998

For: METHOD FOR FABRICATING LOCAL

INTERCONNECT STRUCTURE FOR

INTEGRATED CIRCUIT DEVICES, SOURCE

STRUCTURES (AS AMENDED)

Examiner: T. Nguyen

Group Art Unit: 2813

Attorney Docket No.: 3439.3US (97-936)

CERTIFICATE OF MAILING

I hereby certify that this paper or fee along with any attachments referred to or identified as being attached or enclosed is being deposited with the United States Postal Service as First Class Mail (under 37 C F.R. § 1 8(a)) on the date of deposit shown below with sufficient postage and in an envelope addressed to the Commissioner for Patants, Washington, D.C. 20231.

October 16, 2001 Date of Deposit

Signature of registered practitioner or other person having reasonable basis to expect mailing to occur on date of deposit shown pursuant to 37 C.F.R. § 1.8/aY10.12

Joseph A. Walkowski Typed/printed name of person whose signature is contained above

ASSOCIATE POWER OF ATTORNEY (37 C.F.R. § 1.34(b))

Commissioner for Patents Washington, D.C. 20231

Sir:

In accordance with 37 C.F.R. § 1.34(b), please recognize the following individual as an associate agent/attorney herein in connection with the above-identified patent application, and with all continuing and divisional applications hereof:

Katherine A. Hamer, Reg. No. 47,628

Respectfully submitted,

Joseph A. Walkowski Registration No. 28,765 Attorney for Applicants

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Date: October 16, 2001

JAW/ps:dlm

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